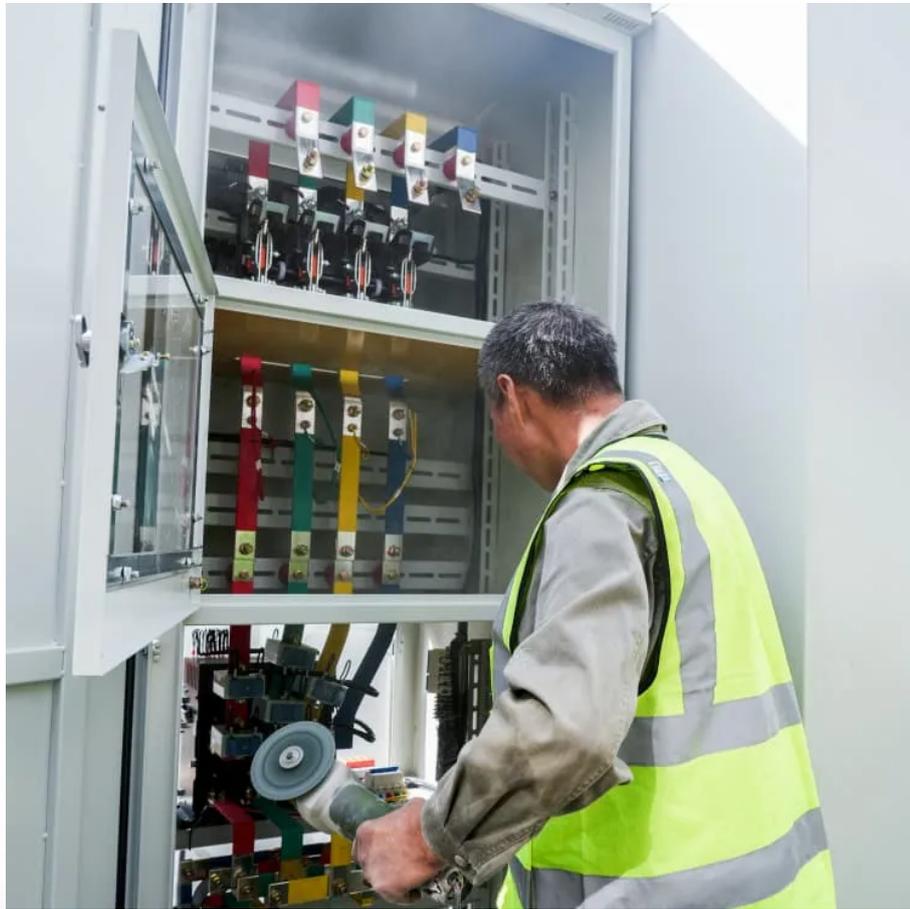


Kongres Container

High voltage protection on the inverter



Overview

How to protect a solar inverter?

A solar inverter must include over-voltage protection, under-voltage protection, short-circuit protection, overload protection, and temperature protection to ensure safe and reliable operation. Q2: How Do I Protect My Inverter?

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Do inverters need protection?

Without proper protection, an inverter can be damaged by power surges, voltage spikes, and other electrical disturbances. There are several types of protection that can be used to protect inverters: Surge protection: This type of protection is designed to protect the inverter from power surges and voltage spikes.

What is inverter power switch short-circuit protection?

Inverter power switch short-circuit protection is fully integrated. A desaturation detection circuit is embedded in both the high- and low-side output stages and monitors the IGBT collector-to-emitter voltage by means of an external high voltage diode.

What types of protection can be used to protect inverters?

There are several types of protection that can be used to protect inverters: Surge protection: This type of protection is designed to protect the inverter from power surges and voltage spikes. Overload protection: This type of protection is designed to protect the inverter from being overloaded.

Why do solar inverters need overvoltage protection?

By protecting the internal circuitry of the inverter from high voltage spikes, overvoltage protection ensures the longevity and reliable operation of the

inverter. This not only extends the life of the inverter but also maintains the efficiency and safety of the entire solar power system.

Why is the protection level at the inverter increased?

In addition, the protection level at the inverter is increased if the overvoltage occurs at one of the other strings. When excessive voltage is applied, voltage falls via the cable inductance. If the arrangement is not ideal, the protection level at the inverter is increased (see Fig. 6).

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